



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Pelloie

SERIAL NO.: 09/890,120

CONF No: 5893

FILING DATE: Feb 4, 2000

TITLE: DYNAMIC THRESHOLD VOLTAGE MOS TRANSISTOR  
FITTED WITH A CURRENT LIMITER

PATENT NO.: 6,787,850

ISSUED: Sep. 7, 2004

EXAMINER: Allan Wilson (Primary) and Edgardo Ortiz (Assistant)

ART UNIT: 2815

Certificate  
JUL 21 2005  
of Correction

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class

Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on the date  
printed below:Date: July 11, 2005 Name: P Sherman  
Penelope ShermanCommissioner for Patents,  
P.O. Box 1450 Alexandria, VA 22313-1450TRANSMITTAL FOR CERTIFICATE OF CORRECTION

We enclose, pursuant to the provisions of 37 C.F.R. §1.322, a Certificate of  
Correction for United States Patent No. 6,787,850. Please make the Certificate of  
Correction and the statements herein of record.

The corrections made to the above-identified United States Patent in the Certificate  
of Correction filed herewith are to correct mistakes which are of a minor character  
according to 35 U.S.C. §254 and 37 C.F.R. §1.322. The proposed corrections do not

constitute such changes in the patent as would constitute new matter or would require re-examination.

37 C.F.R. §1.322 Corrections

Please see attached Certificate of Correction.

No Fee Due

It is believed that no fee is required for filing the above-noted document. In the event any fee is required for filing of this Certificate of Correction, the Assistant Commissioner is hereby authorized to charge the fee to our Deposit Account No. 50-1698.

Respectfully submitted,  
THELEN REID & PRIEST LLP



Robert Krebs  
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Dated: July 11, 2005

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# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO : 6,787,850

DATED : Sep 7, 2004

INVENTOR(S) : Pelloie

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

- 1) In column 6 line 66 after the transistors formed, replace "a re" with --are--.

MAILING ADDRESS OF SENDER:  
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PATENT NO. 6,787,850

No. of additional copies

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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